



Docket No.: 250316US2

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/797,083

Applicants: Yasuhiko KURIYAMA

Filing Date: March 11, 2004

For: BIAS CURRENT SUPPLY CIRCUIT AND
AMPLIFICATION CIRCUIT

Group Art Unit: 2817

Examiner: Choe, Henry

SIR:

Attached hereto for filing are the following papers:

PROVISIONAL ELECTION

LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

TWO (2) REPLACEMENT DRAWING SHEETS

Our credit card payment form in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

22850

(703) 413-3000 (phone)
(703) 413-2220 (fax)

IFW

OBLON

SPIVAK

McCLELLAND

MAIER

& NEUSTADT

P.C.

ATTORNEYS AT LAW

ECKHARD H. KUESTERS
(703) 413-3000
EKUESTERS@OBLON.COM

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Yasuhiko KURIYAMA

SERIAL NO.: 10/797,083

GAU: 2817

FILED: March 11, 2004

EXAMINER: CHOE, HENRY

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COMMISSIONER FOR PATENTS

Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

2 Replacement Drawing Sheets _____ New Drawing Sheets

Official Action dated July 19, 2005

Notice of Allowance/Issue Fee dated _____

Other dated _____

The changes and/or modifications made include the following:

Figures 1-4 have been labeled --PRIOR ART-- in accordance with the requirement noted in the Office Action dated July 19, 2005.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters
Registration No. 28,870

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 06/04)